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For the
S-100 bus

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WITH 16-BIT WORD LENGTH

8086 CPU
This card brings state-of-the-art performance to the S-100 bus. It may be used to upgrade existing 8-bit systems by "swapping" the CPUs or it may form the foundation for a high performance 16-bit system. It will operate with 8-bit, 16-bit, or mixed memory and peripherals. It has a 1-megabyte addressing range. It can be factory upgraded at nominal cost from 4 MHz. to 8 MHz. when the faster CPU chip is available. Price — $895.

CPU Support Card
This is a companion to our 8086 CPU. It includes a 2K monitor with machine language debugger and disk bootstrap loader, serial port with software-selected baud rate, time-of-day clock with battery backup capability, two general purpose timers/counters, and a vectored interrupt controller with 7 interrupts generated on board and 8 accepted from the bus. Price — $395.

8/16 Memory Card
Through the use of the sXTRQ line of the proposed IEEE Standard, this memory board will appear to be 8K by 16 bits to our 8086 CPU or 16K by 8 bits to 8-bit CPUs. It is offered with 250 nsec. memory chips only and will perform without wait states with our 8086 CPU using an 8 MHz. clock. It has 24-bit extended addressing. Price — $595.

Z80/8086 Cross Assembler
This cross assembler runs under CP/M and its derivatives. Its mnemonics are the same as or similar to Intel's ASM-86. It is available in 5" soft-sectored, 5" North Star, or 8" soft-sectored (IBM) formats. Price — $250.

Microsoft BASIC-86
Microsoft's BASIC interpreter for the 8086 is essentially identical in features to their 5.0 release for the 8080 and is ANSI compatible. It is a "stand-alone" version and includes all disk and terminal I/O drivers. Programs written for any earlier version of Microsoft BASIC will run under BASIC-86 with little or no modification. Price — $350.

MCS-86 User's Manual
By Intel — Feb., 1979, edition. This is the primary hardware and software reference manual for the 8086 CPU. Price — $6.25. (Includes shipping)

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